# Surface Mount Schottky Power Rectifier

## **SMA Power Surface Mount Package**

... employing the Schottky Barrier principle in a metal-to-silicon power rectifier. Features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for low voltage, high frequency switching power supplies; free wheeling diodes and polarity protection diodes.

- Compact Package with J-Bend Leads Ideal for Automated Handling
- Highly Stable Oxide Passivated Junction
- Guardring for Over-Voltage Protection
- Optimized for Low Leakage Current

#### **Mechanical Characteristics:**

- Case: Molded Epoxy
- Epoxy Meets UL94, V<sub>O</sub> at 1/8"
- Weight: 70 mg (approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Polarity: Polarity Band Indicates Cathode Lead
- Available in 12 mm Tape, 5000 Units per 13 inch Reel
- ESD Protection: Human Body Model > 4000 V (Class 3) Machine Model > 400 V (Class C)
- Marking: B1L2

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RBM</sub> V <sub>RWM</sub> V <sub>R</sub>	20	V
Average Rectified Forward Current (At Rated V <sub>R</sub> , T <sub>L</sub> = 110°C)	lo	1.0	A
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I <sub>FSM</sub>	40	A
Storage/Operating Case Temperature Operating Junction Temperature	T <sub>stg</sub> , T <sub>C</sub> T <sub>J</sub>	–55 to +125	°C
Voltage Rate of Change (Rated V <sub>R</sub> , T <sub>J</sub> = 25°C)	dv/dt	10,000	V/µs



## **ON Semiconductor®**

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SCHOTTKY BARRIER RECTIFIER 1 AMPERE 20 VOLTS

B1L2 = Device Code

## ORDERING INFORMATION

SMA

CASE 403D

PLASTIC

Device	Package	Shipping	
MBRA120LT3	SMA	5000/Tape & Reel	

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	<b>5 mm x 5 mm</b> (Note 2)	1 Inch x 1/2 inch (Note 3)	Unit
Thermal Resistance – Junction-to-Lead	Psi <sub>JL</sub>	34	20	°C/W
	(Note 4)			
Thermal Resistance – Junction-to-Ambient	$R_{\theta JA}$	138	77	

#### **ELECTRICAL CHARACTERISTICS**

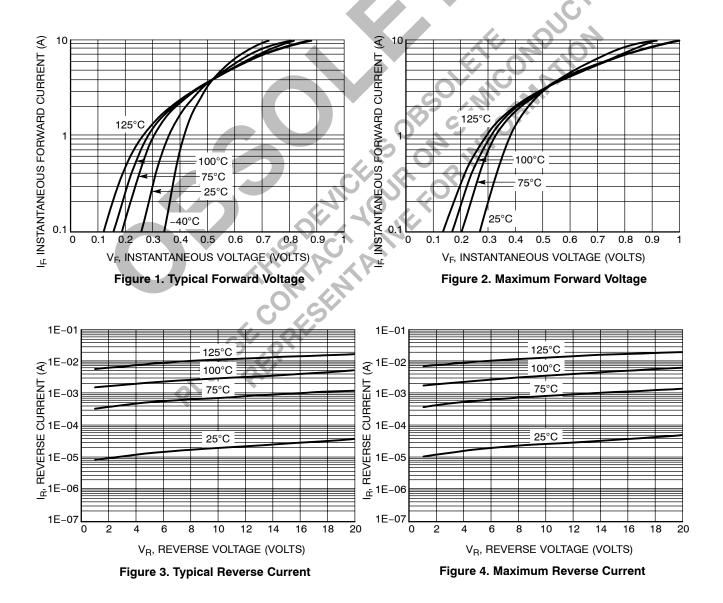
Maximum Instantaneous Forward Voltage (Note 1), See Figure 2	V <sub>F</sub>	T <sub>J</sub> = 25°C	T <sub>J</sub> = 125°C	V
(I <sub>F</sub> = 0.1 A) (I <sub>F</sub> = 1.0 A) (I <sub>F</sub> = 2.0 A)		0.300 0.395 0.445	0.15 0.30 0.40	
Maximum Instantaneous Reverse Current, See Figure 4	I <sub>R</sub>	T <sub>J</sub> = 25°C	T <sub>J</sub> = 100°C	mA
(V <sub>R</sub> = 20 V) (V <sub>R</sub> = 10 V)		0.2 0.1	6.0 4.0	

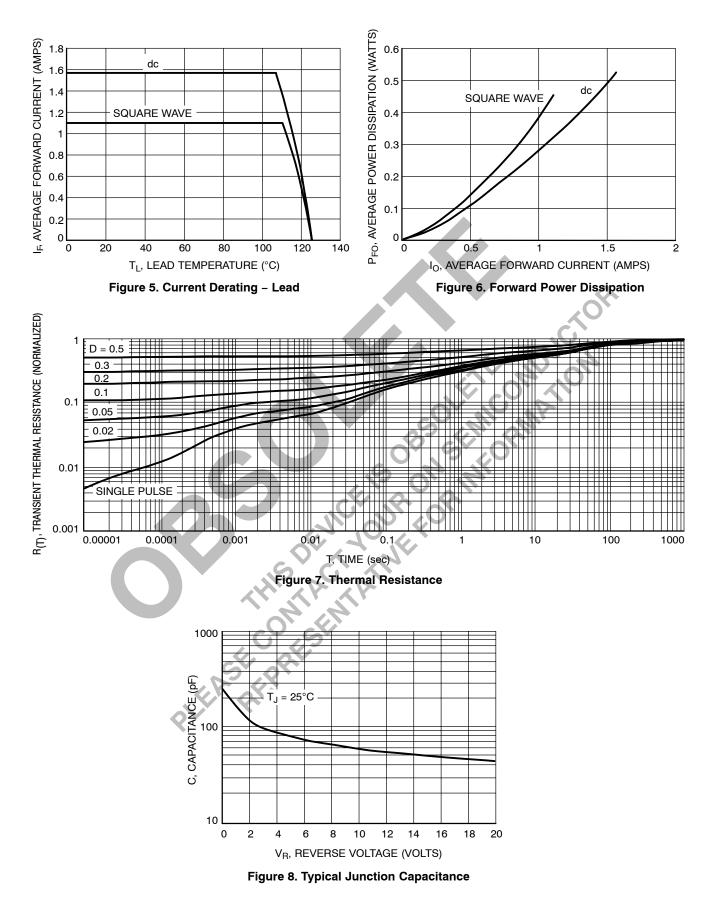
1. Pulse Test: Pulse Width ≤ 250 μs, Duty Cycle ≤ 2%.

2. Mounted on a Pad Size of 5 mm x 5 mm, PC Board FR4 (2 pads).

3. Mounted on a Pad Size of 1 inch x 1/2 inch, PC Board FR4 (2 pads).

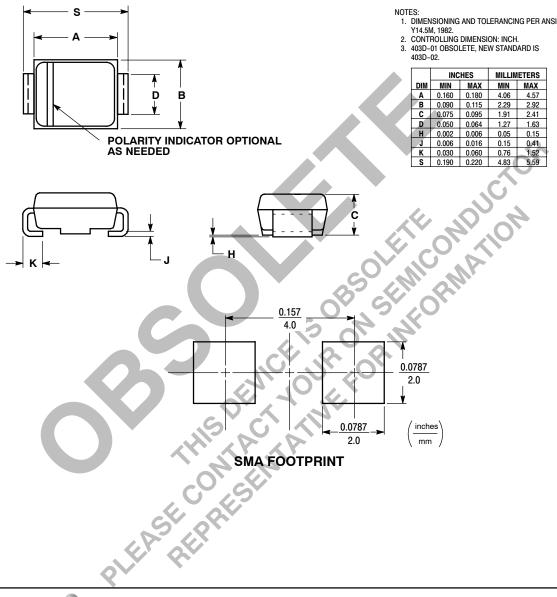
4. In compliance with JEDEC 51, these values (historically represented by  $R_{\theta JL}$ ) are now referenced as  $Psi_{JL}$ .





#### PACKAGE DIMENSIONS

SMA CASE 403D-02 ISSUE A



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